09/886225
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JC F-JTT

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
SCANNED TR2 Q.A.	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/886225	F	707 714	718	3661 2133	Tsuy

APPLICANTS

Chen-Tsai Lee

TITLE

Memory interlace-checking method

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip Inside File Jacket.

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.	NOTICE OF ALLOWANCE MAILED
(Assistant Examiner) (Date)	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	**ISSUE FEE**
	Amount Due	Date Paid
(Primary Examiner) (Date)	
☐ The terminal ____ months of this patent have been disclaimed.	ISSUE BATCH NUMBER
(Legal Instruments Examiner) (Date)	

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